

10 772 159 C-9C  
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INTELLECTUAL PROPERTY LAW

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SAN JOSE, CA  
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DENVER, CO  
PORTLAND/LAKE OSWEGO, OR

April 5, 2010

Attention: Certificate of Corrections Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Certificate**  
**APR 12 2010**  
**of Correction**

Re: **CERTIFICATE OF CORRECTION**  
U. S. Letters Patent No.: 7,022,965 B2  
Issued: April 4, 2006  
For: LOW VOLTAGE ACTIVE CMOS PIXEL ON AN N-TYPE  
SUBSTRATE WITH COMPLETE RESET  
Our File No.: 4320P064X

Dear Sir:

Enclosed is a Certificate of Correction (two copies) for the above-referenced patent.

This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C. Section 254.

Respectfully submitted,

BLAKELY SOKOLOFF TAYLOR & ZAFMAN LLP

Andrew J. Cameron

AJC/kam

Enclosures: PTO Form- 1050-Amended

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**UNITED STATES PATENT AND TRADEMARK OFFICE**  
**CERTIFICATE OF CORRECTION**

**PATENT NO:** 7,022,965 B2

**DATED:** April 4, 2006

**INVENTOR(S):** Sohei Manabe and Hidetoshi Nozaki

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page, item [73], Assignee: delete "Tehnologies" and insert --Technologies--.

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**MAILING ADDRESS OF SENDER:**  
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Sunnyvale, CA 94085-4040

**PATENT NO.** 7,489,120 B2

**Certificate of Correction (PTO Form 1050)-Amended**

1 of 1

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